29.   (previously presented)   The processor of claim 28, wherein the first subset of cache lines are associated with a processor instruction cache.
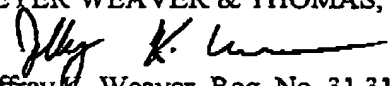
30.   (previously presented)   The processor of claim 28, wherein the plurality of instructions invalidates substantially all of the lines associated with processor cache.

REMARKS

No new matter has been added. This amendment is being filed to correct typographical errors in the claims. Applicant hereby respectfully requests that on recommendation of the Examiner and approval by the Director that this amendment be entered. If there are any questions with regards to this matter, please do not hesitate to contact at us at the number below.

The Commissioner is authorized to charge any fees beyond the amount enclosed which may be required, or to credit any overpayment, to Deposit Account No. 500388 (Order No. ALTRP129).

<div style="text-align: right">

Respectfully submitted,
BEYER WEAVER & THOMAS, LLP

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